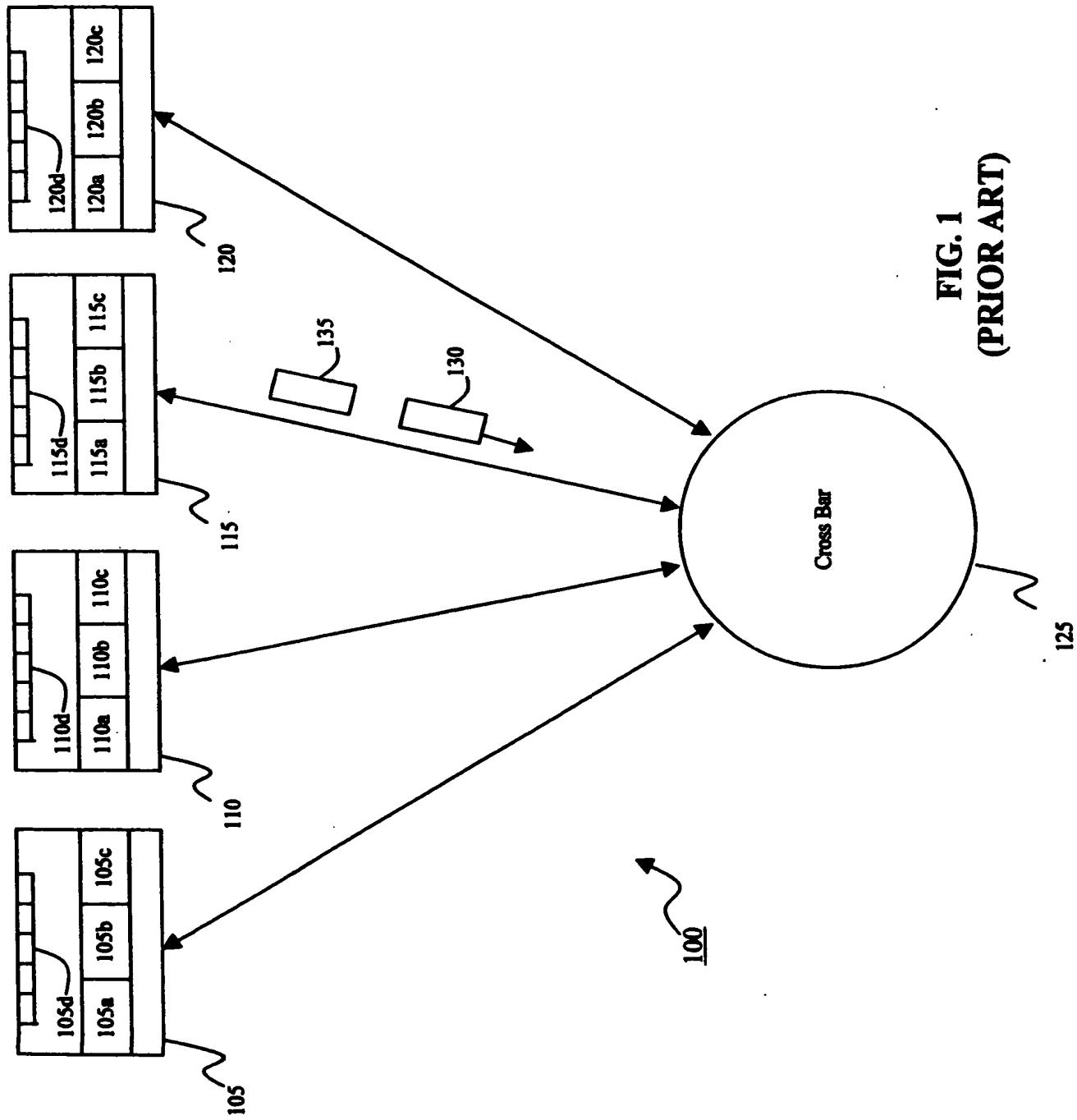


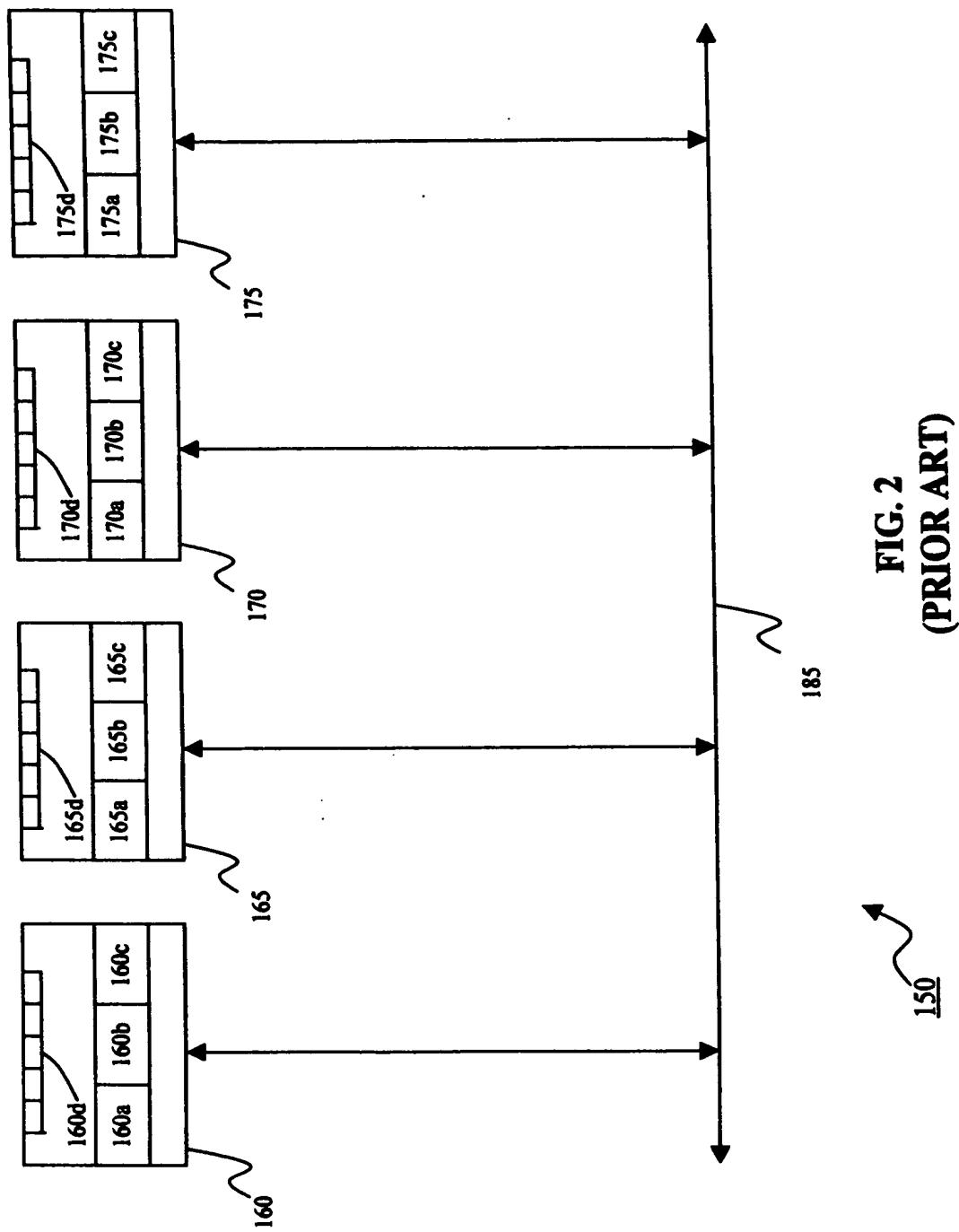
Title: DISTRIBUTED SWITCH MEMORY ...

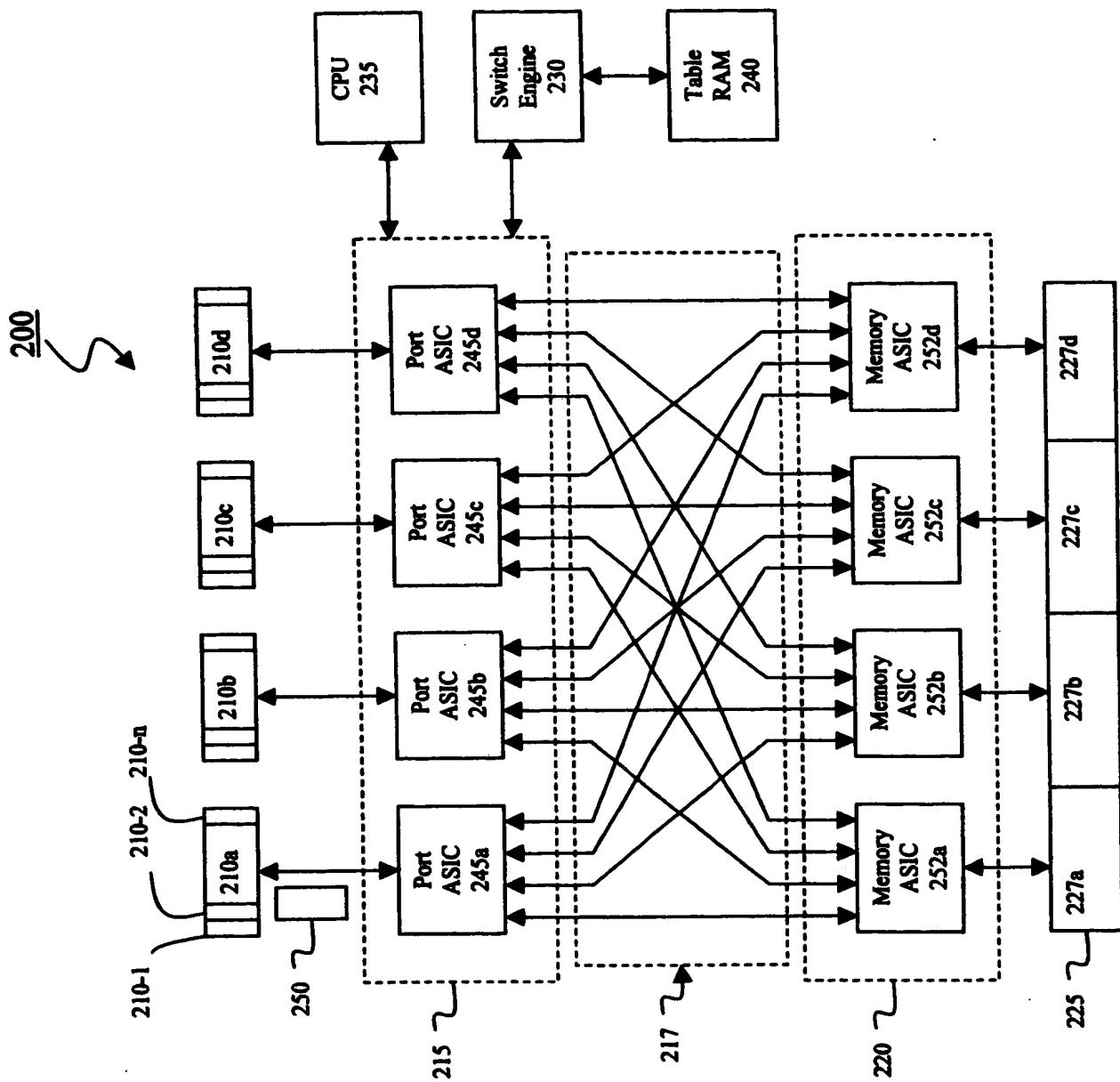
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FIG. 1  
(PRIOR ART)



**FIG. 3**

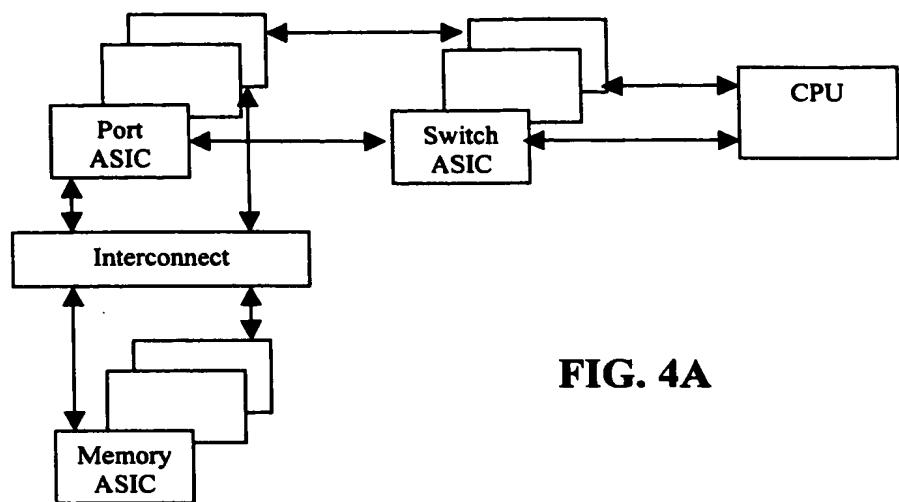


FIG. 4A

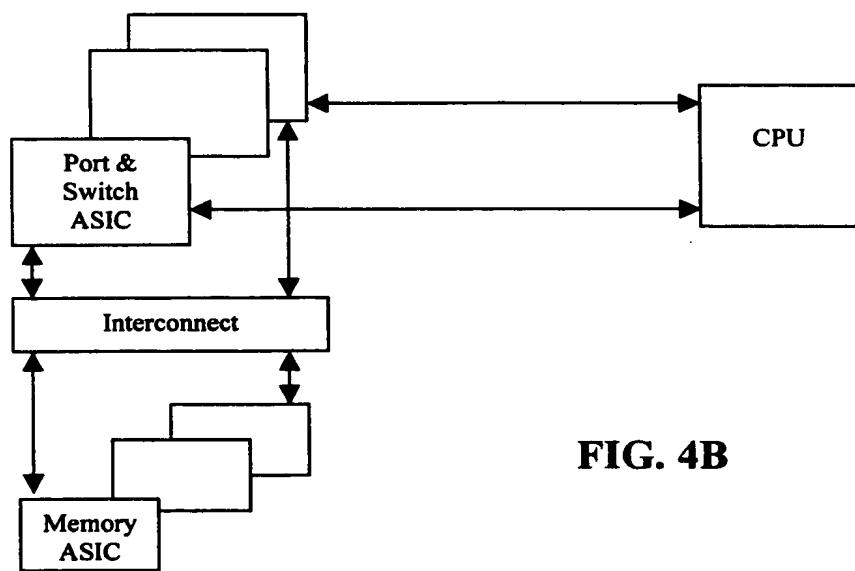


FIG. 4B

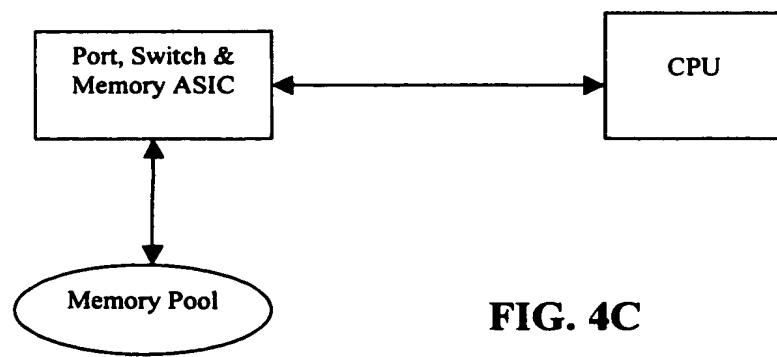
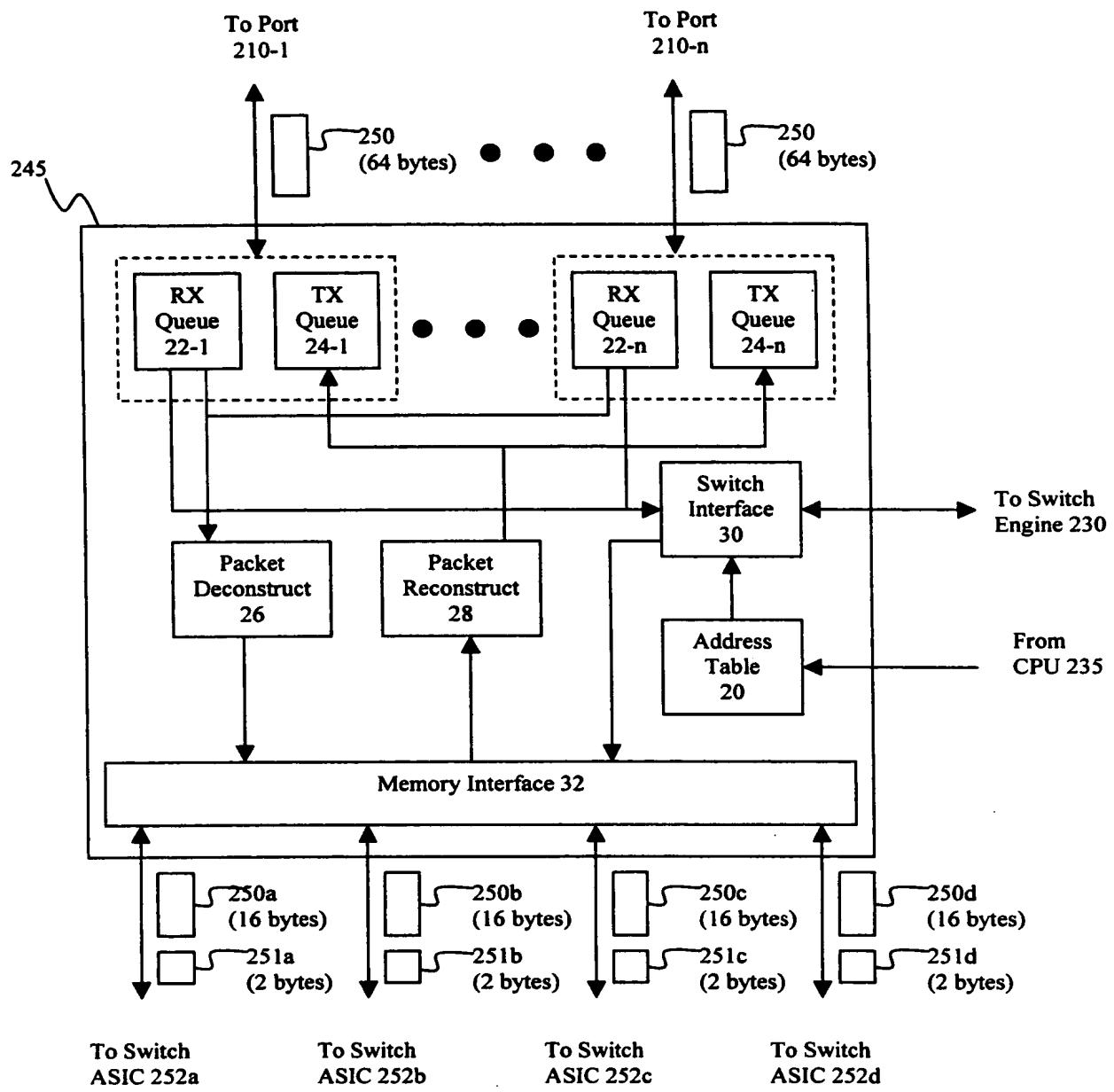


FIG. 4C

**FIG. 5**

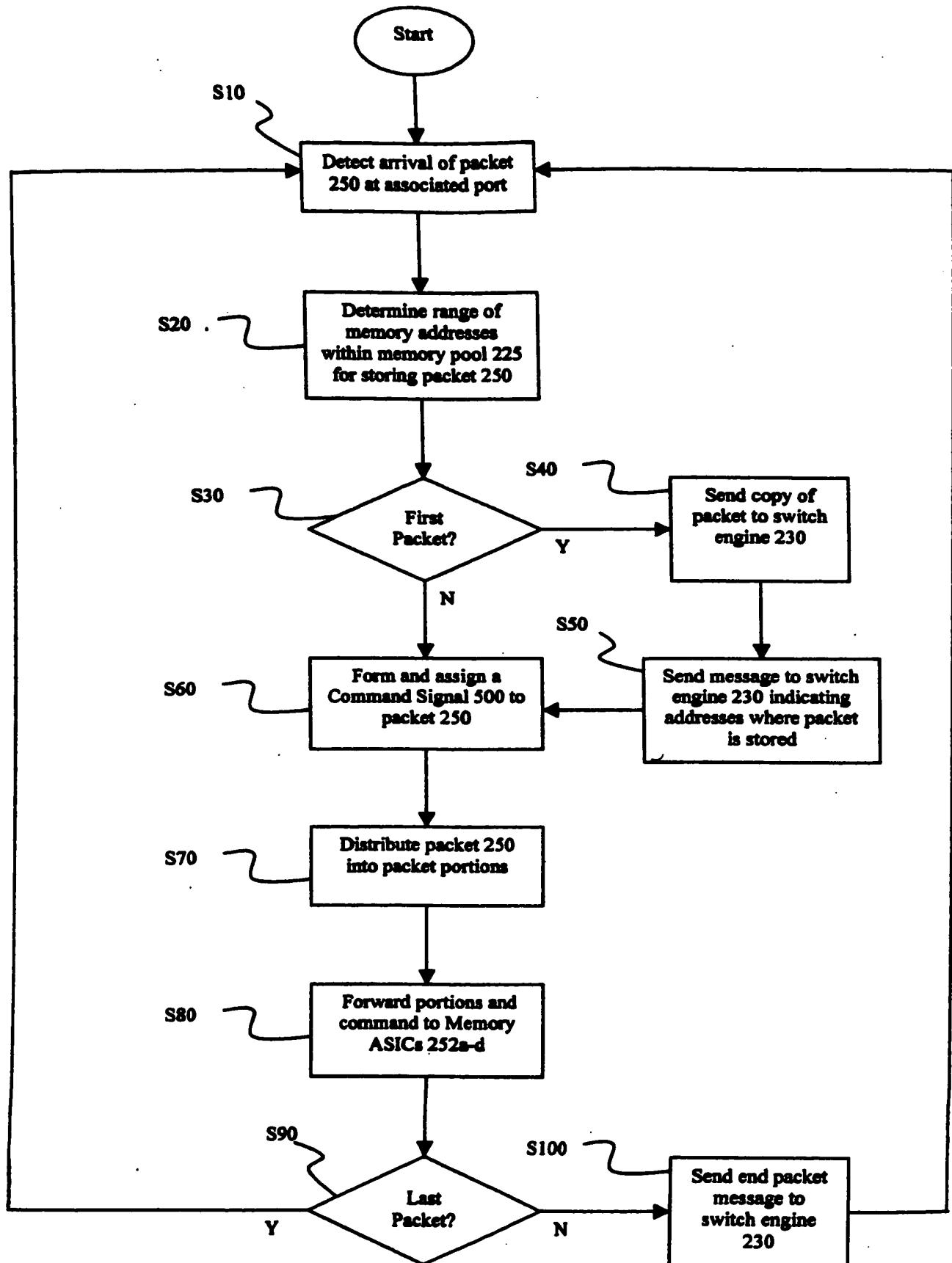
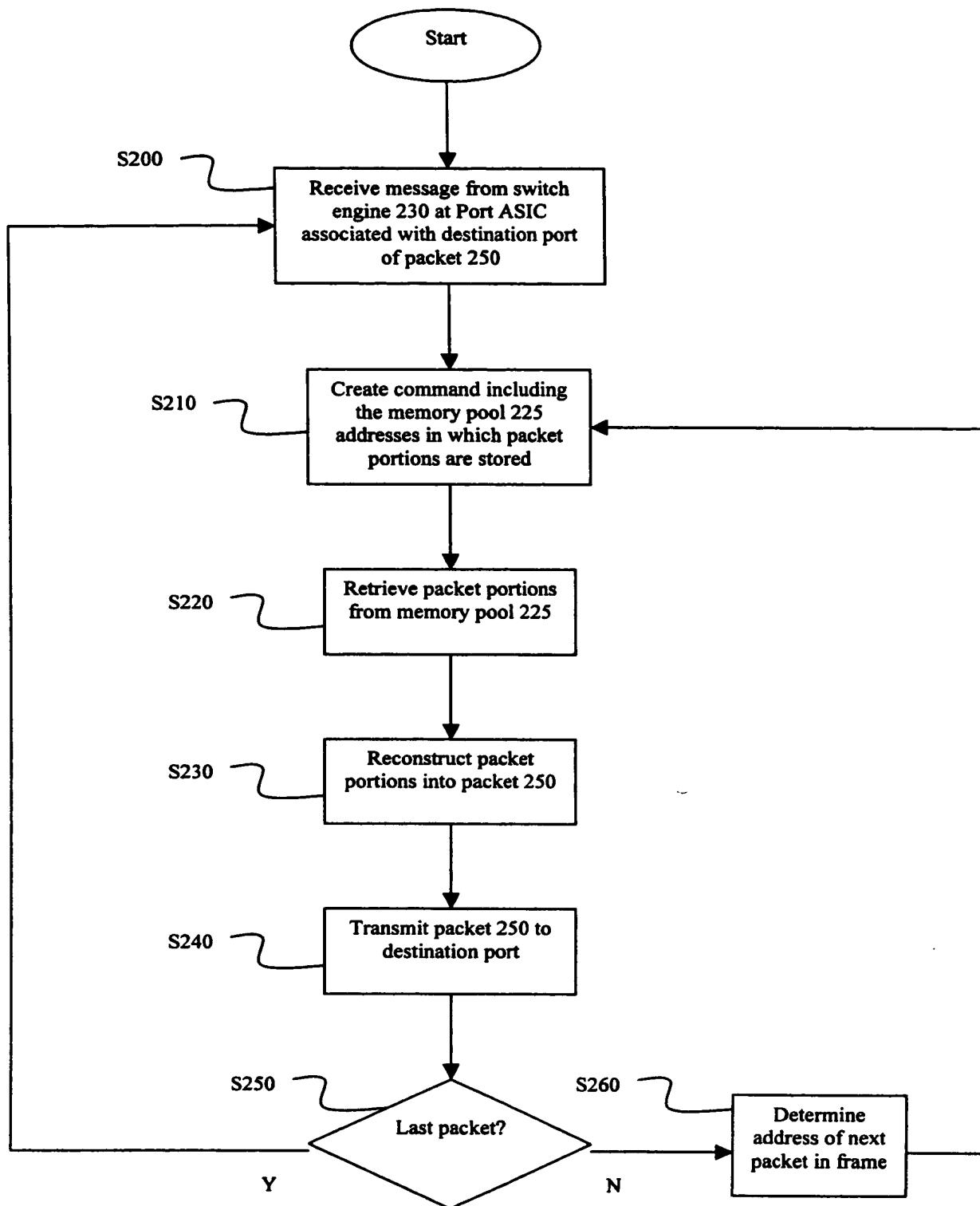


FIG. 6



**FIG. 7**

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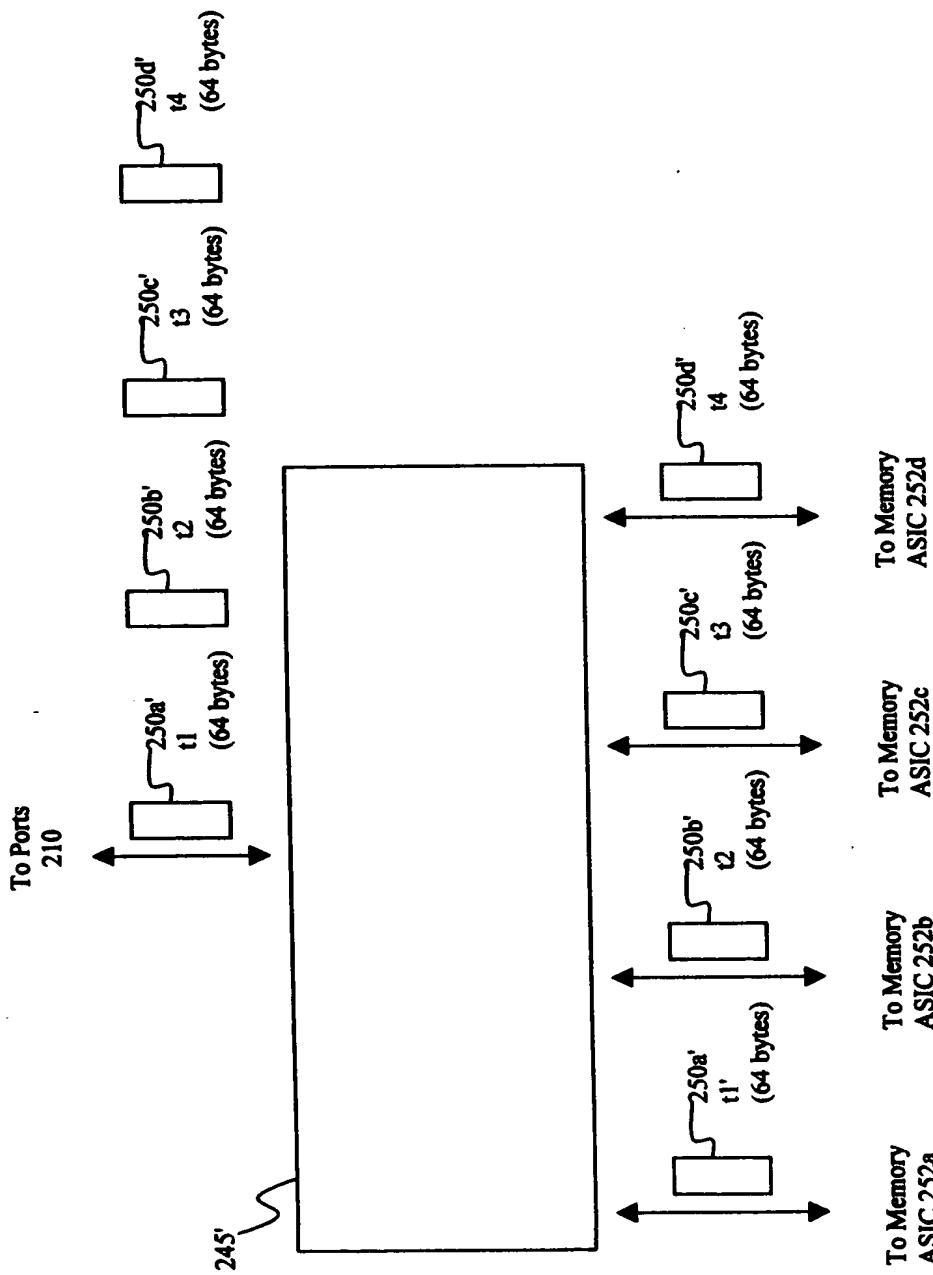
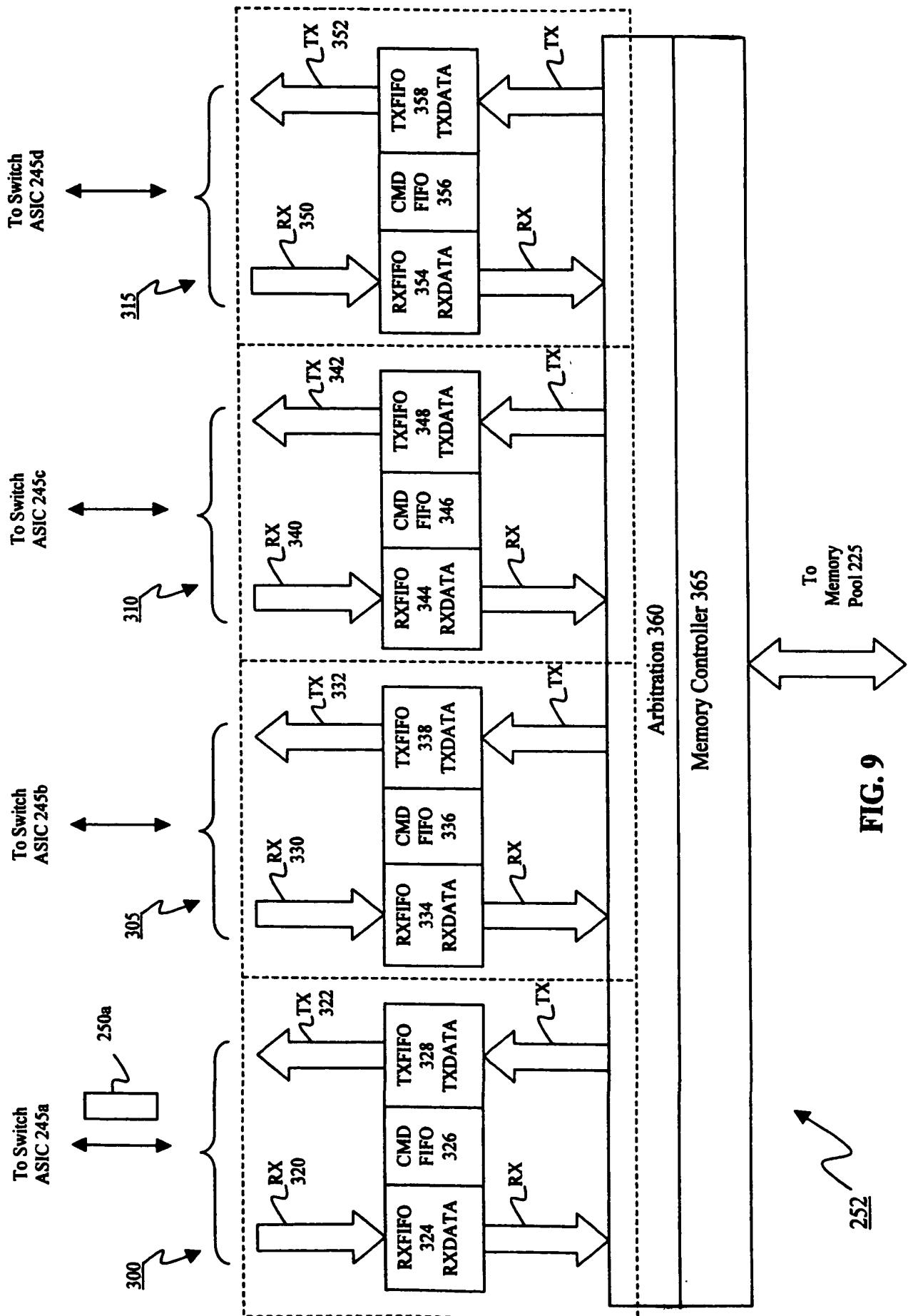


FIG. 8

**FIG. 9**

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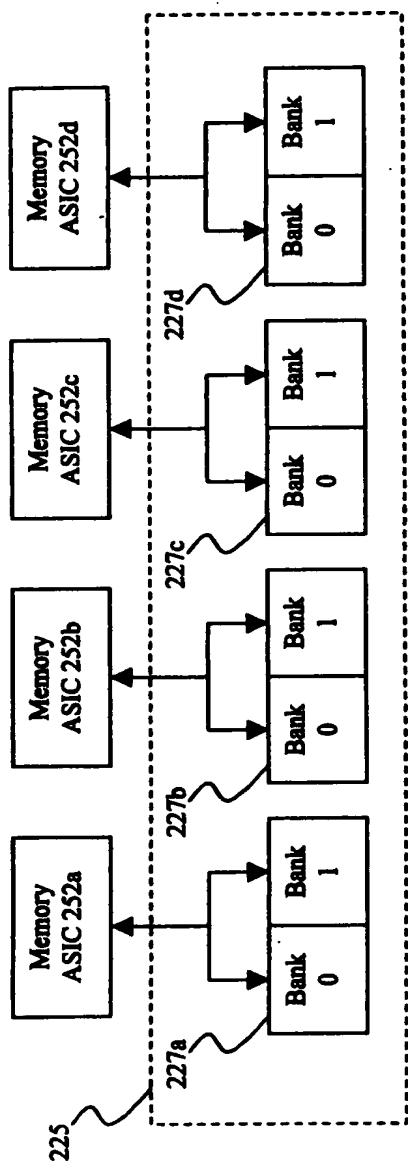


FIG. 10A

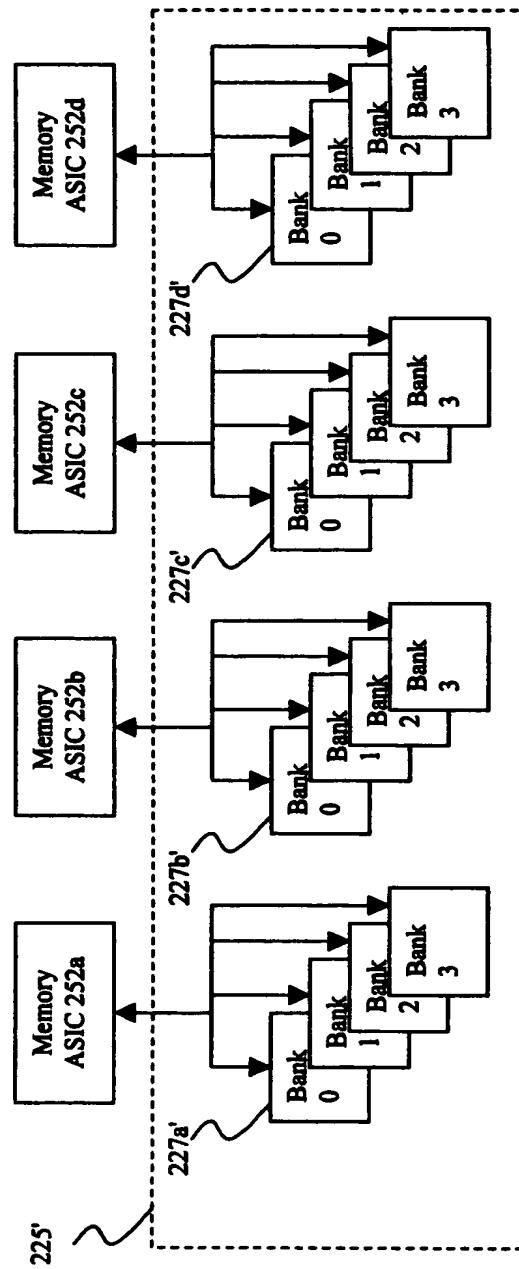


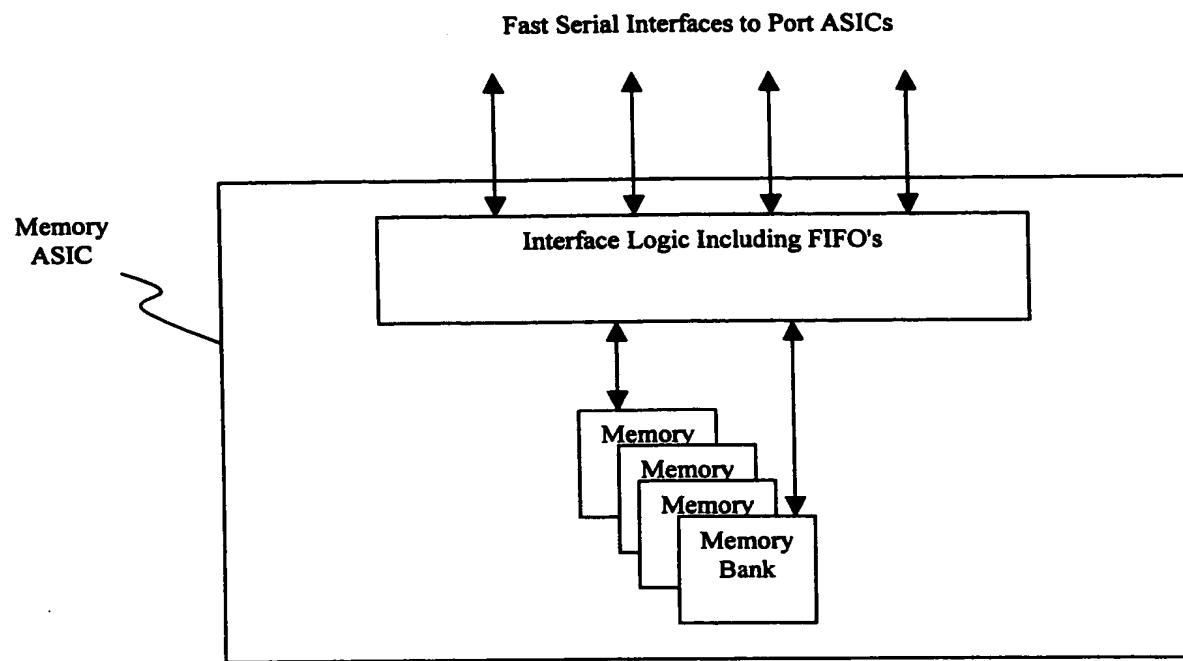
FIG. 10B

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**FIG. 11**

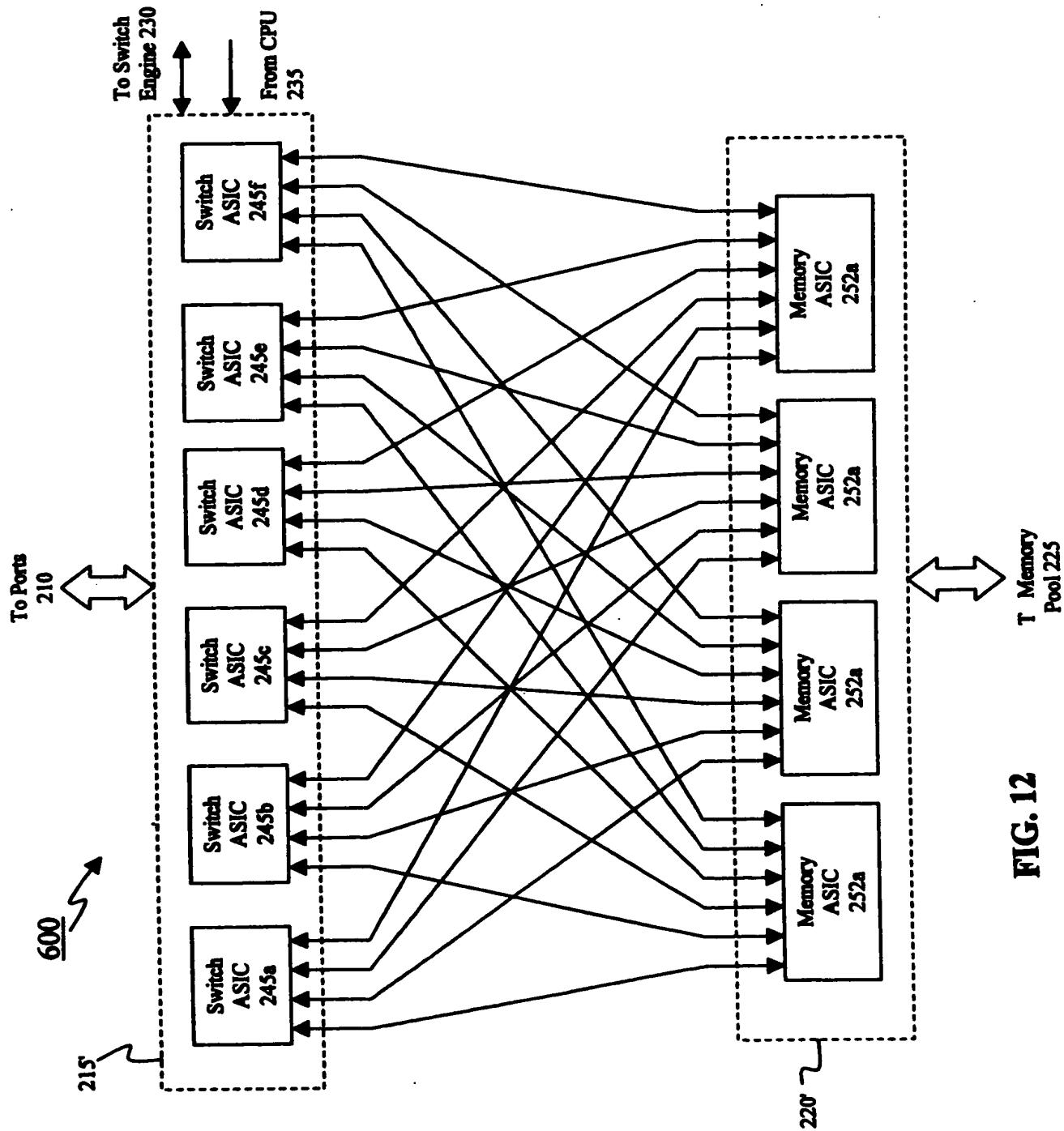


FIG. 12